



Breaking News:

Tarari, AMD Demonstrate Acceleration Technology

WASHINGTON, DC, March 22 -- Tarari, Inc., the award-winning acceleration company and proven leader in deep content inspection silicon, today demonstrated a stunning example of the future of accelerated computing. Tarari's latest T10 Technology will be featured in an opening keynote address delivered by AMD Chairman and CEO Hector Ruiz at 9 am today at the Federal Office Systems Expo (FOSE) show in Washington, D.C.

With the advent of Tarari T10 Technology Acceleration engines, customers using Dual-Core AMD Opteron processors can expect to see Anti-Virus scanning ability jump from 300Mbps into the 6 to 10Gbps range -- a more than 20-fold improvement while using significantly less CPU cycles.

"Our mutual customers continue to see the need for comprehensive Layer 7 Network Security and XML processing with AMD's Direct Connect Architecture," said Marty Seyer, senior vice president, Commercial Segment and Advanced Solutions, AMD. "Tarari's T10 acceleration engines are a great example of how AMD's Torrenza open platform innovation initiative can help accelerate the pace of innovation and result in new levels of benefits for customers."

Tarari's T10 Technology represents the next generation of multi-core Content Processing ASICs and will natively support AMD Opteron processors and both the HyperTransport technology and PCI-Express interfaces. This allows Tarari's silicon to be optimally interconnected in low latency, high bandwidth configurations with AMD Opteron processors. Tarari's "Content Processor" products are ASICs, production boards, and embedded software components that are designed to snap in to networking, appliance, blades, and server systems.

"Together Tarari and AMD have made significant progress since announcing Tarari's commitment to the AMD Torrenza initiative and to AMD Opteron processors in June of last year," said Gary Smerdon, Chief Marketing Officer at Tarari, Inc. "T10 Technology is here today and we will be ramping our production quantities in Q2."

Developers can immediately start building their designs using Tarari's silicon since Tarari supports a standard API across all of its software and silicon products that is transparent as to the "processor to content processor" interconnect method used. Tarari's Network Security, XML/Web Services and Digital Media Development kits enable customers to immediately develop cost-effective, leading-edge systems with enhanced functionality, proven interoperability and improved time-to-market.

Together Tarari Content Processors and AMD Opteron processors provide a comprehensive content inspection solution with a common application programming interface (API) all the way from 10 Megabits per Second (Mbps) to 10 Gigabits per second (Gbps). HyperTransport technology is an optimized interconnect technology delivering lowest possible latency, highest bandwidth, design flexibility, performance scalability and broad extensibility to other popular interconnect architectures and will help Tarari Content Processors and AMD64 processors with Direct Connect Architecture transfer data at more than 50Gbps.

About Tarari, Inc.

Tarari, the award-winning acceleration company headquartered in San Diego, Calif., USA, designs and develops Content Processor silicon to allow original equipment manufacturers (OEMs) to accelerate and offload compute-intensive, complex algorithms used in XML/Web Services, Network Security and Digital Media environments. Tarari is a fabless semiconductor company whose product line primarily includes ASICs, and Tarari also offers boards and software acceleration components designed for network equipment, appliance and server vendors, as well as independent software vendors. Tarari is a privately held company with venture backing from Crosspoint Venture Partners, Morgan Stanley Venture Partners, Enterprise Partners Venture Capital, Miramar Venture Partners, XMLFund and Intel Capital. For more information, visit www.tarari.com, e-mail: info@tarari.com or call 858-385-5131.
